BSC025N03MS G-VB Datasheet N-Channel 30 V (D-S) MOSFET

FEATURES



Parameter	Symbol	Test Conditions	Min .	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		35		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	ι _D = 250 μΑ		- 7.5		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.5		2.5	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			Α
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 32 \text{ A}$		0.0018		Ω
		$V_{GS} = 4.5 \text{ V}, I_D = 29 \text{ A}$		0.0025		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 32 A	160			S
Dynamic ^b			l	<u> </u>		
Input Capacitance	C _{iss}				9900	pF
Output Capacitance	C _{oss}	$V_{DS} = 12.5 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$			1725	
Reverse Transfer Capacitance	C _{rss}				970	
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 32 \text{ A}$			83	nC
					82	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 29 \text{ A}$			34	
Gate-Drain Charge	Q_{gd}				29	
Gate Resistance	R _g	f = 1 MHz		1.4	2.1	Ω
Turn-On Delay Time	t _{d(on)}			18	27	ns
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_{L} = 0.555 \Omega$		11	17	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 27$ A, V_{GEN} = 10 V, R_g = 1 Ω		70	105	
Fall Time	t _f			10	15	
Turn-On Delay Time	t _{d(on)}			55	83	
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_{L} = 0.625 \Omega$		180	270	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 24 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		55	83	
Fall Time	t _f			12	18	
Drain-Source Body Diode Characteristic	s		l	<u> </u>		
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			100	- A
Pulse Diode Forward Current ^a	I _{SM}				200	
Body Diode Voltage	V _{SD}	I _S = 22 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			52	78	ns
Body Diode Reverse Recovery Charge	Q _{rr}	1 00 A diddy 400 A/2- T 05 00		70.2	105	nC
Reverse Recovery Fall Time	t _a	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		27		ns
Reverse Recovery Rise Time	t _b			25		

Notes:

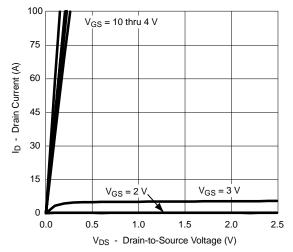
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

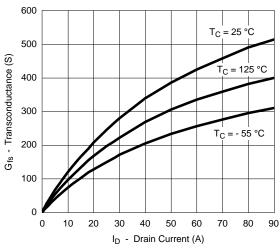
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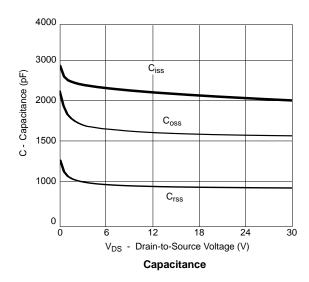
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

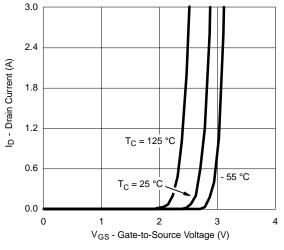


Output Characteristics

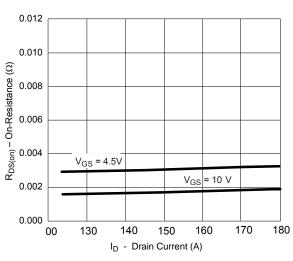


Transconductance

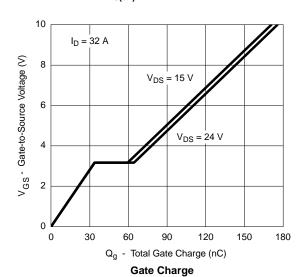




Transfer Characteristics



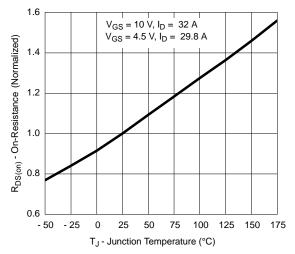
 $R_{DS(on)}$ vs. Drain Current



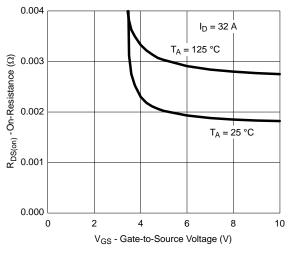
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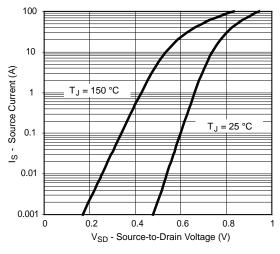
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



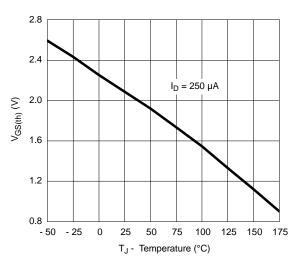
On-Resistance vs. Junction Temperature



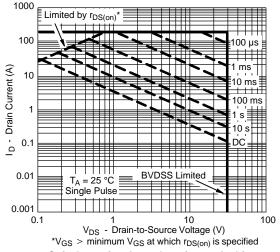
 $R_{DS(on)}$ vs. V_{GS} vs. Temperature



Forward Diode Voltage vs. Temperature



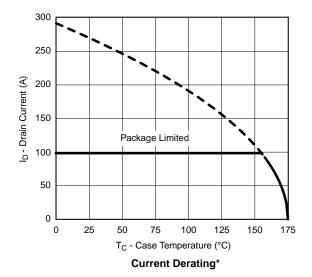
Threshold Voltage

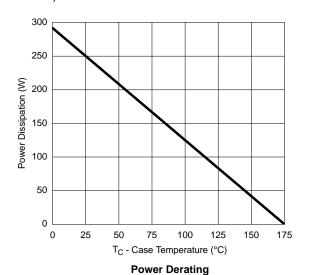


Safe Operating Area, Junction-to-Ambient

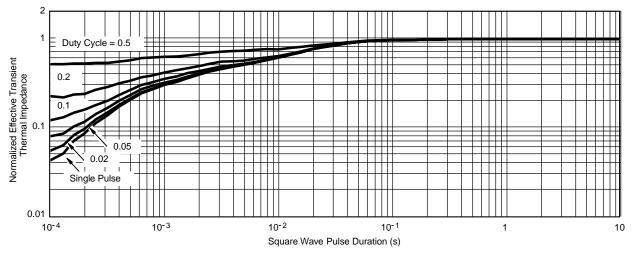


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



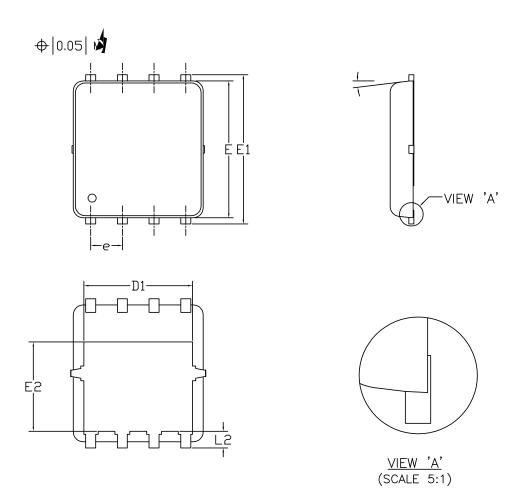


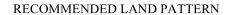
* The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

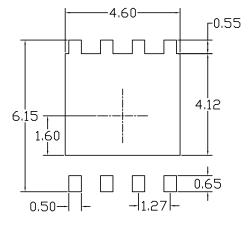


Normalized Thermal Transient Impedance, Junction-to-Case

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	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SYMBOLS	X1					MAX	
	MIN	NOM	MAX	MIN	NOM	MAA	
A	0.85	0. 95	1.00	0.033	0.037	0.039	
A1	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.010	0.020	
С	0.15	0. 20	0. 25	0.006	0.008	0.010	
D		5. 20			0. 205		
D1		4. 35			0.171		
Е		5. 55			0.219		
E1		6.05			0. 238		
E2		3. 625			0. 143		
e	1. 27 BSC			0.050 BSC			
L	0.45	0. 55	0.65	0.018	0.022	0.026	
L1	0		0.15	0		0.006	
L2	0.68 REF			0.027 REF			
6	0°		10°	0°		10°	

NOTE

- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
 MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- 2. CONTROLLING DIMENSION IS MILLIMETER.
 CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

UNIT: mm



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